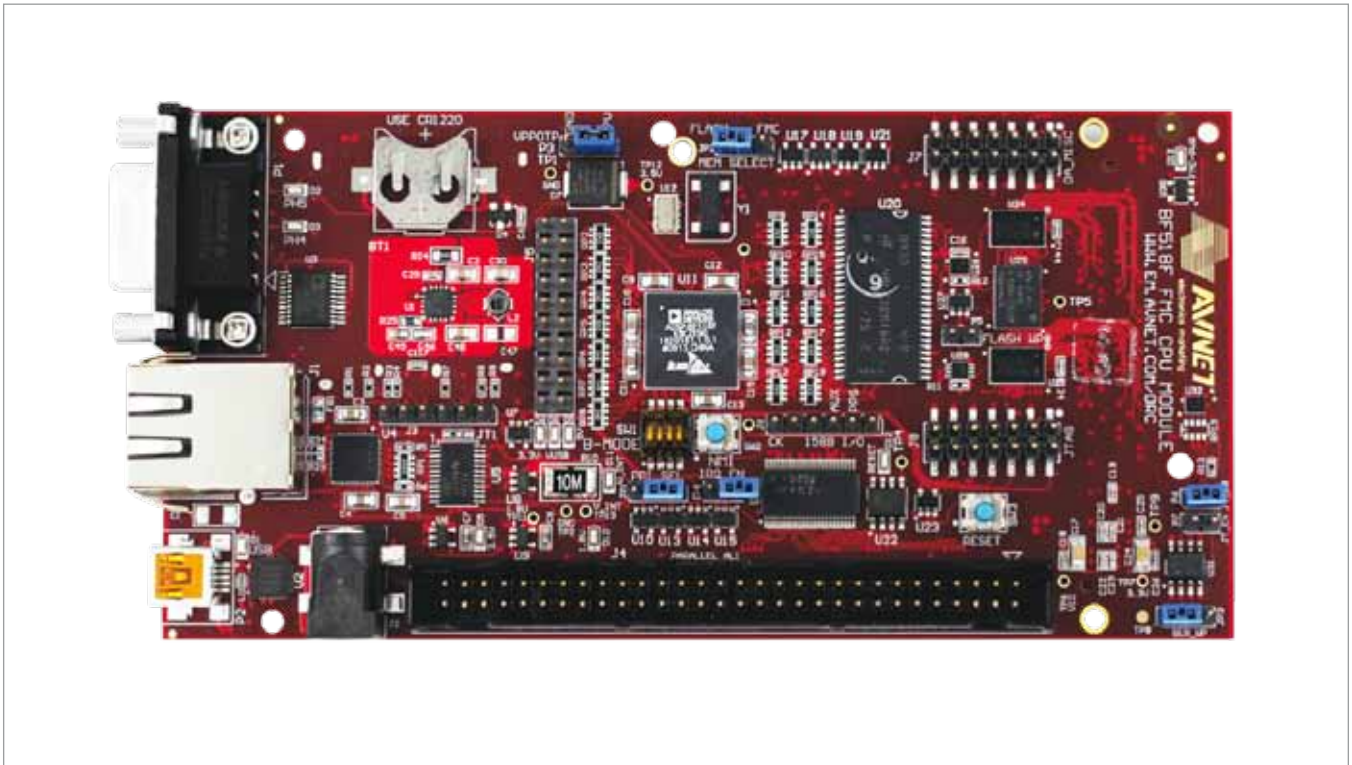


Analog Devices BF518F FMC

DEVELOPMENT KIT



The Analog Devices BF518F FMC Development Kit, designed by Avnet Electronics Marketing and in Europe available exclusively from Silica, provides a simple-to-use platform for hardware and software engineers designing with the Analog Devices BF518F low-power Blackfin[®] processor and the Microsoft .NET Micro Framework. The board can operate either as a standalone, or as an FMC Mezzanine Module connected to any VITA57 compliant carrier board. By providing a convenient modular platform for applications that combine FPGAs, embedded processors with advanced networking capability, and the familiar Visual Studio software development environment, it has never been easier to get a jump start on your next co-processing design.

Target Applications

- Industrial automation HMI
- Smart energy management
- Network-enabled instrumentation
- Test and measurement

Features

- Operates as a standalone, or as a VITA 57 FMC Mezzanine Module
- Microsoft .NET Micro Framework ported by Adeneo Embedded
- 400 MHz ADSP-BF518F Blackfin processor
- 64 MB Micron PC-133 SDRAM
- 4 MB Numonyx parallel NOR Flash
- 4 MB SPI Flash (internal to CPU)
- Flexible booting options
- 10/100 Ethernet with IEEE 1588 support
- SD/MMC card slot
- One RS-232 port (DB-9F)
- One USB 2.0 mini-AB port
- Supports external TFT-LCD conforming to Avnet LCD Interface (ALI) specification
- Optional add-on debug agent

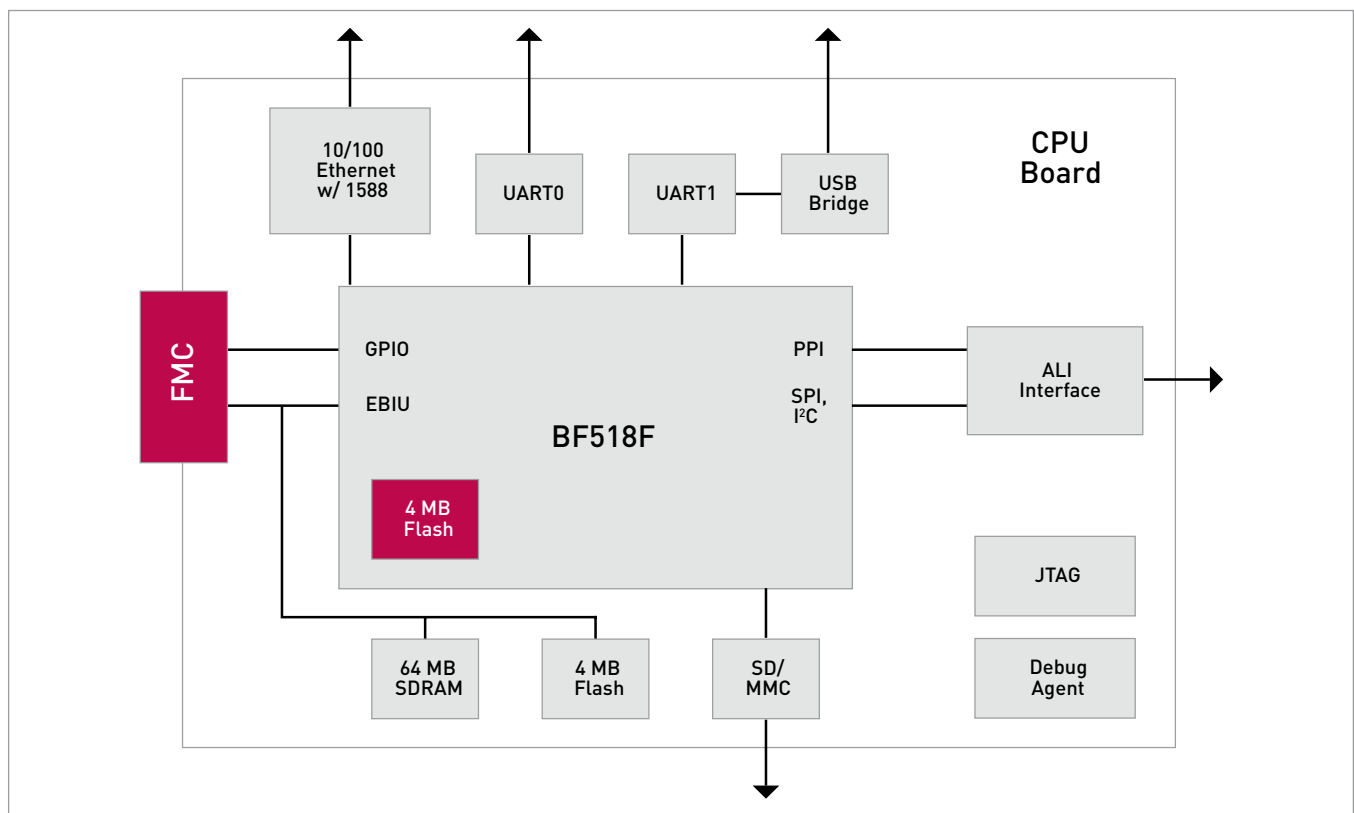


Analog Devices BF518F FMC

DEVELOPMENT KIT

Kit Content

- BF518F-based FMC processor board
- Microsoft .NET Micro Framework ported by Adeneo Embedded
- USB cable
- Analog Devices standalone debug agent (SKU option)
- Downloadable design documentation and reference design



Featured Manufacturers



Ordering Information

Part Number	Hardware	Resale
AES-FMC-BF518F-G	Analog Devices BF518F FMC Development Kit	\$ 349
AES-FMC-BF518F-DA-G	Analog Devices BF518F FMC Development Kit with add-on debug agent board	\$ 499

For more information, visit www.silica.com/AES-FMC-BF518F